



## Firmware Control sheet

	<b>Drg</b>	<b>StarLite SLPC</b>	
	<b>Description</b>	StarLite transceiver IEEE 802.15.4 coordinator for Pixie module	
<b>Issue</b>	<b>ECN</b>	<b>Date</b>	<b>Reason for Change</b>
<b>1</b>	<b>n/a</b>		<b>First Issue</b>

Device		18LF4620	
Design Requirements			
Feature		(Default)	Design / Customer Requirement
Ident. Mark		Dot	
Oscillator			HS
Fail safe clock mon			Disabled
Internal External Oscillator Switch Over			Disabled
Power Up Timer			Disabled
B.O.D.			Enabled while active, SOBREN disabled
B.O.Voltage.			2.0V
Watchdog			Disabled, controlled by SWDTEN
Watchdog Postscaler			1:512
CCP2 Mux			RB3
Port B A/D Enable			Digital I/O on reset
Low power timer 1			Disabled
Master Clear Enable			MCLR Enabled
Stack Overflow Reset			Enabled
LV Program			Disabled
Code Protect			Disabled
Data EE Protect			Disabled
REad protect			Disabled
Write protect			Disabled
B'gnd. debug			Disabled
Serialisation?		Yes	Contact FlexiPanel for a SQTP file of unique MAC addresses

<b>Checksum</b>	
<b>Other Requirements</b>	